

Beyond the Brick Wall of Complexity: Design Technologies for a New Era

16-20 January 2012, EPFL Lausanne, Switzerland



	Monday, 16 Dec 2012	Tuesday, 17 Dec 2012	Wednesday, 18 Dec 2012	Thursday, 19 Dec 2012	Friday, 20 Dec 2012
8:30-10:00	CAD challenges in a mobility driven era Noel Menezes <i>Intel Corporation</i>	Capturing, Modeling and Leveraging Process Variability in IC Design Costas Spanos <i>University of California, Berkeley</i>	Special Presentation: 9:30-10:00 The Swiss Nano-Tera Program Martin Rajman <i>Nano-Tera Executive Director</i>	No lecture!	Scaling Trends of Energy Efficient, Low Voltage Operations David Blaauw <i>University of Michigan, Ann Arbor</i>
10:00-10:30	Coffee & Discussion	Coffee & Discussion	Coffee	Coffee	Coffee & Discussion
10:30-12:00	Device and Circuit Considerations for sub-22nm Technologies Kaushik Roy <i>Purdue University</i>	Design technologies for next-generation heterogeneous many-core platforms Luca Benini <i>University of Bologna</i>	The Early Days of CMOS VLSI Design and CAD; Back to the Future Steve Kang <i>University of California, Santa Cruz</i>	Energy Efficient Designs with Wide Dynamic Range Vivek De <i>Intel Corporation</i>	Measuring and enhancing the reliability of CMOS circuits Sachin Sapatnekar <i>University of Minnesota, Minneapolis</i>
12:00-12:30	Discussion	Discussion	Discussion	Discussion	Discussion
12:30-14:00	Lunch	Lunch	Lunch	Lunch	Lunch
14:00-15:30	ASIP: The (too) long way from academia to product Heinrich Meyr <i>RWTH Aachen University of Technology</i>	Static Timing for the Other 99% Larry Jones <i>Synopsys Inc.</i>	Can Higher-Level Abstraction Help in Crossing the Brick Wall of Complexity? Rob Roy <i>Atrenta Inc.</i>	Protocol Verification in SOC Designs – An Industrial Experience John Moondanos <i>University of Thessaly</i>	Are guardbanding and overdesign viable under 20nm? The case of voltage integrity George Stamoulis <i>University of Thessaly</i>
15:30-16:30	Coffee & Discussion	Coffee & Discussion	Coffee & Discussion	Coffee & Discussion	Coffee & Discussion

More at <http://si.epfl.ch/DesignTechnologies>